

Figure 1

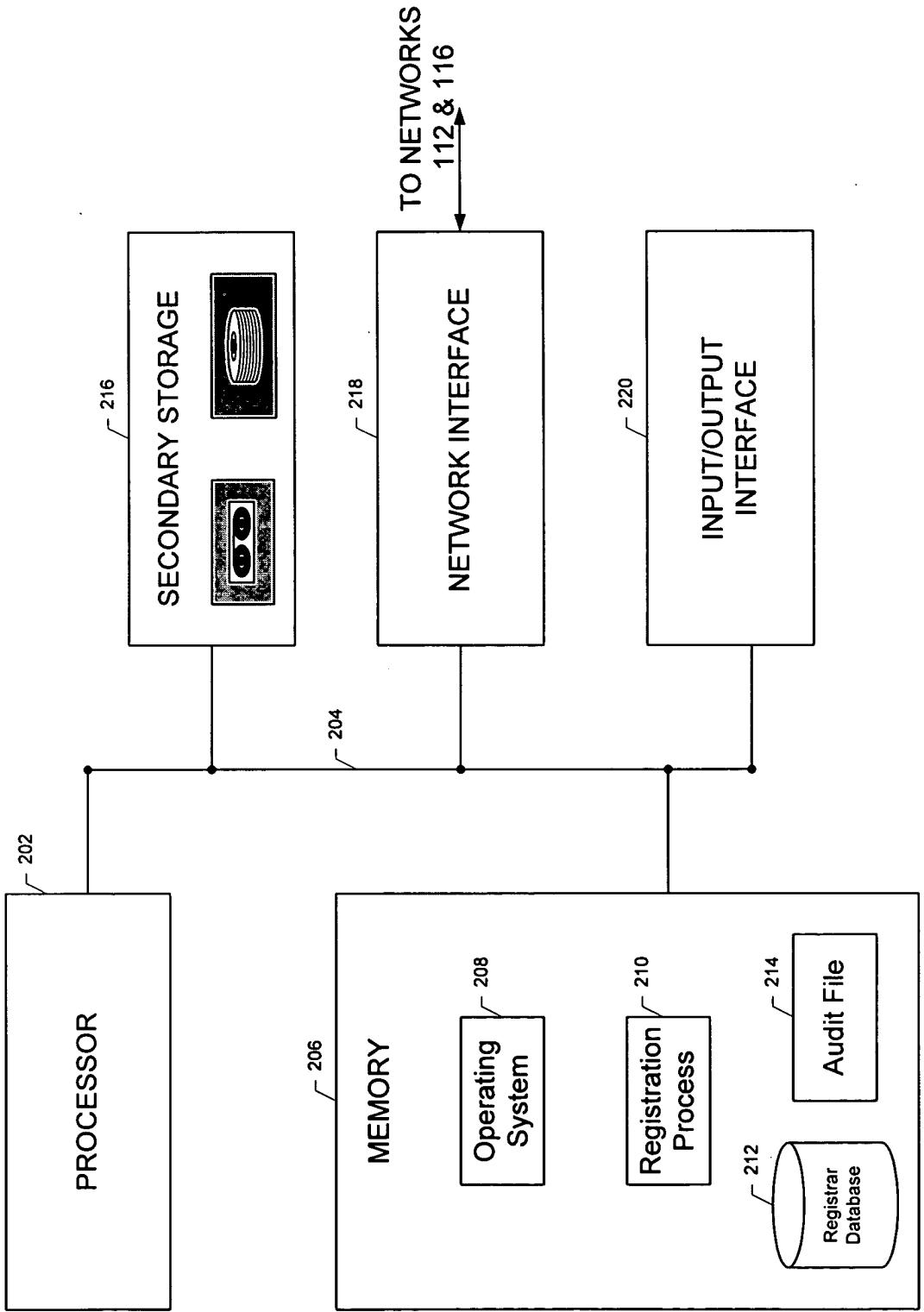


Figure 2

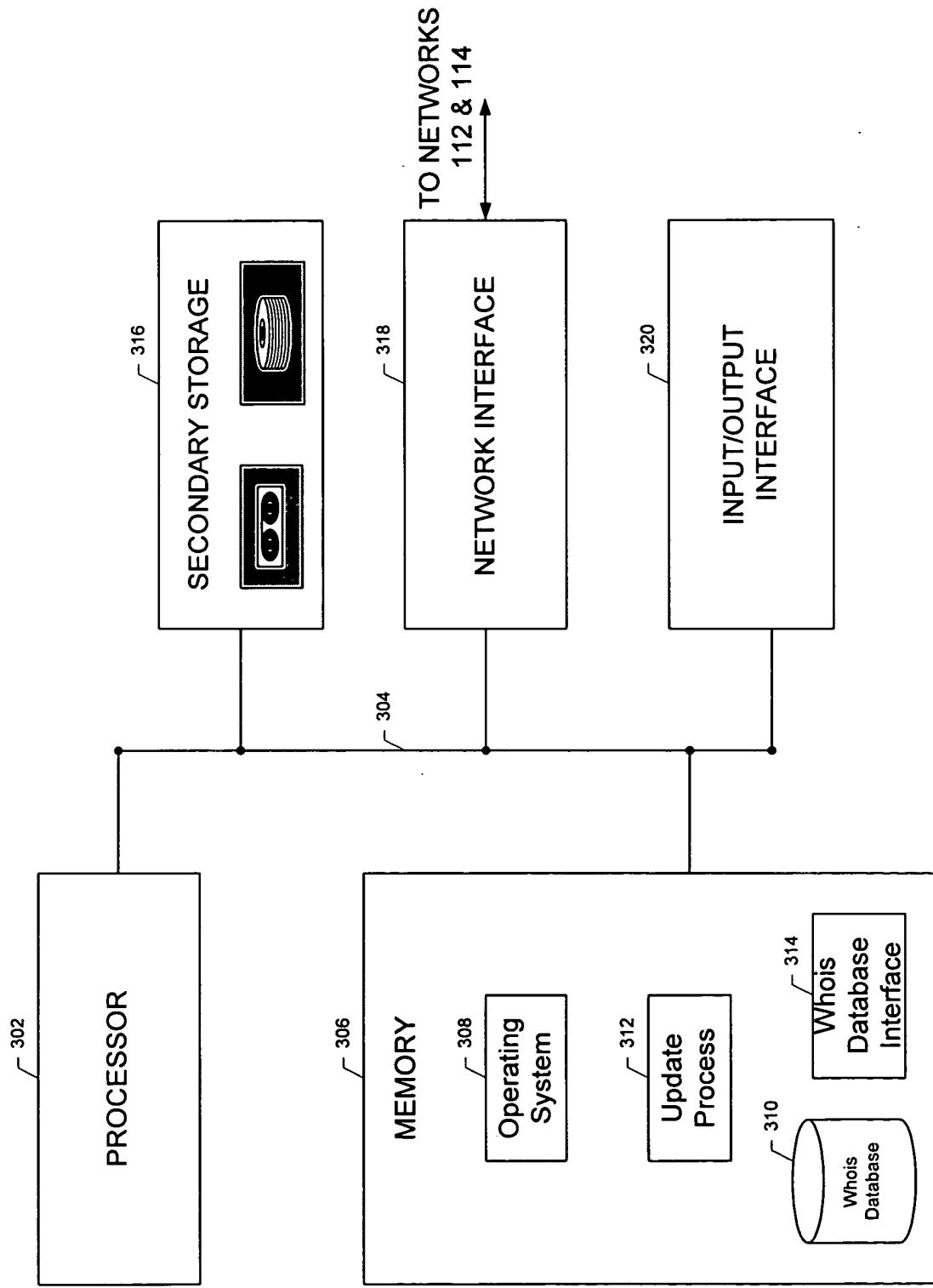


Figure 3

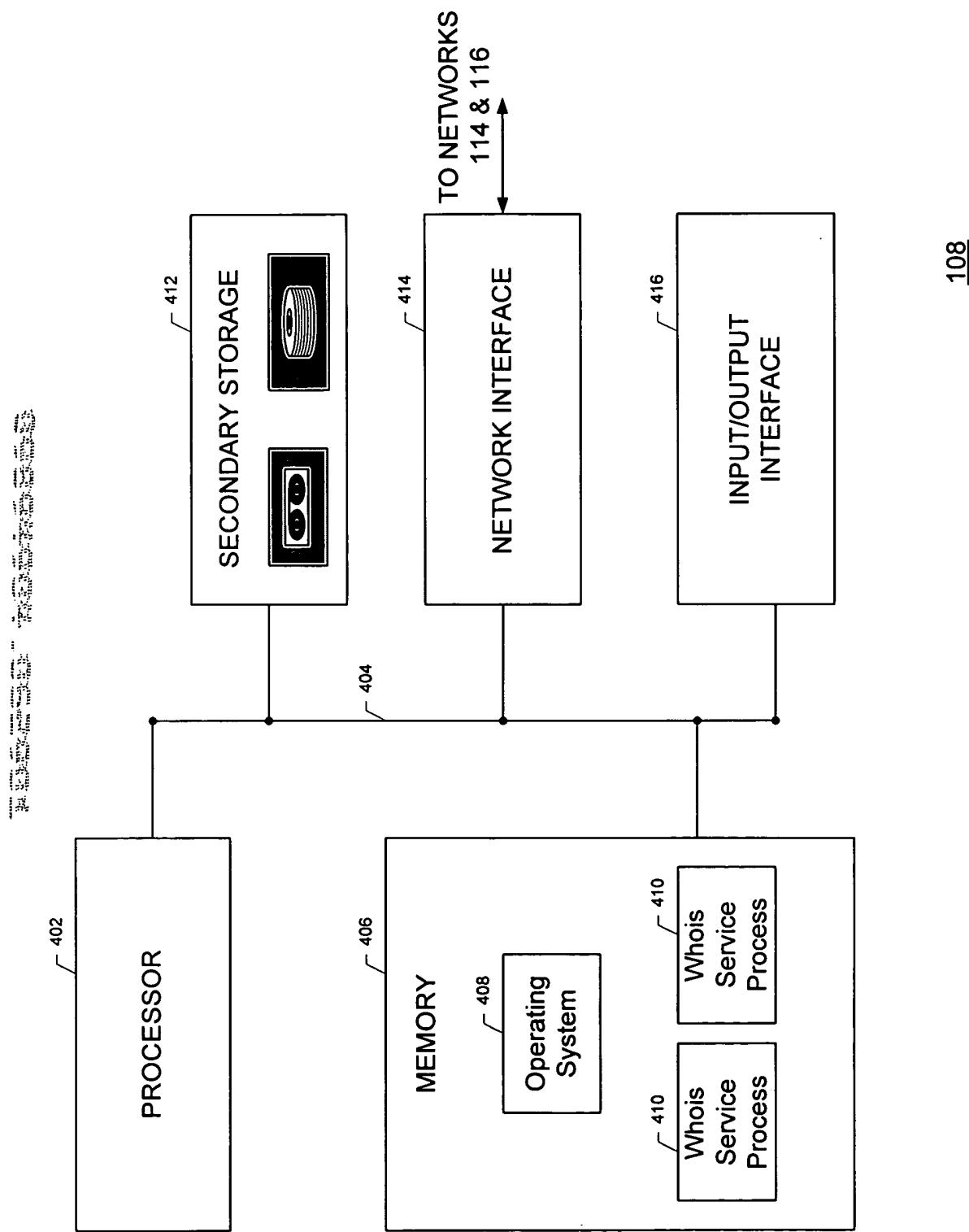


Figure 4

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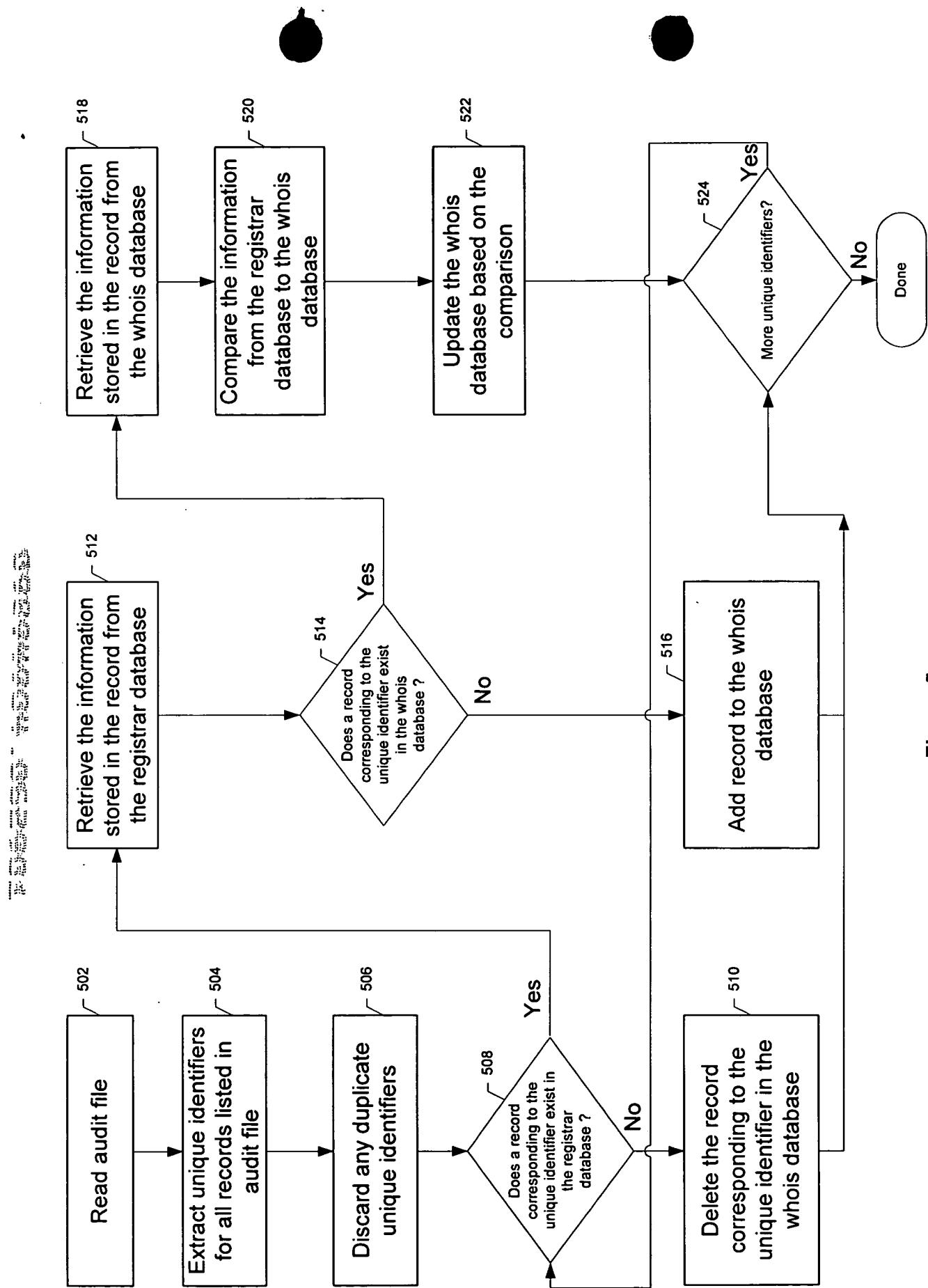


Figure 5

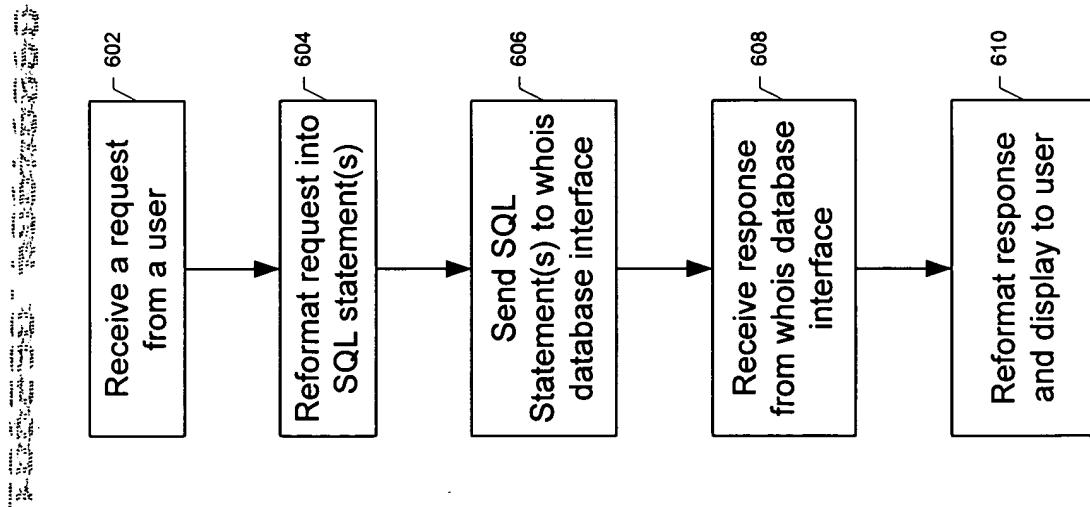


Figure 6

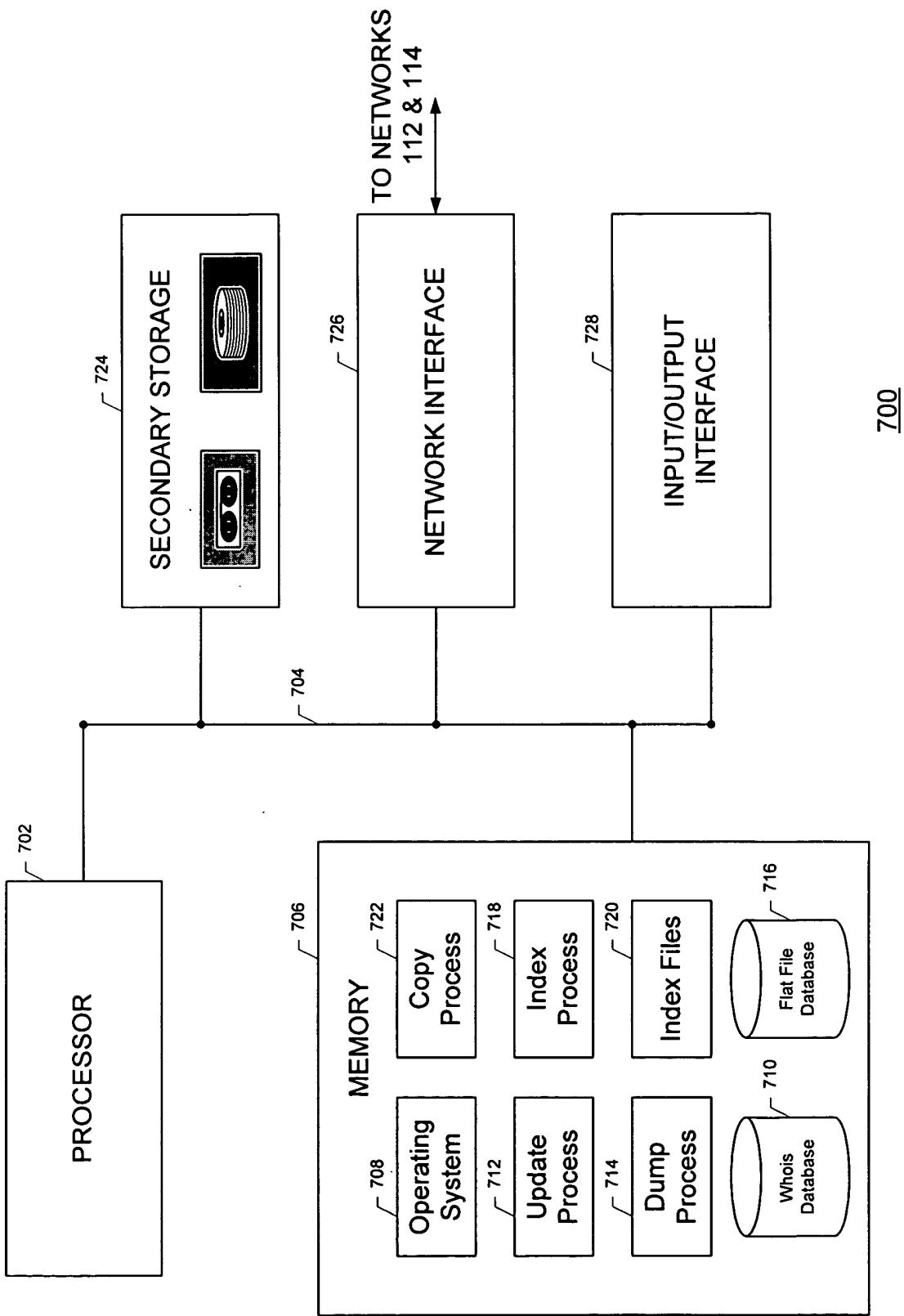


Figure 7

Processor → Secondary Storage → Network Interface → Input/Output Interface

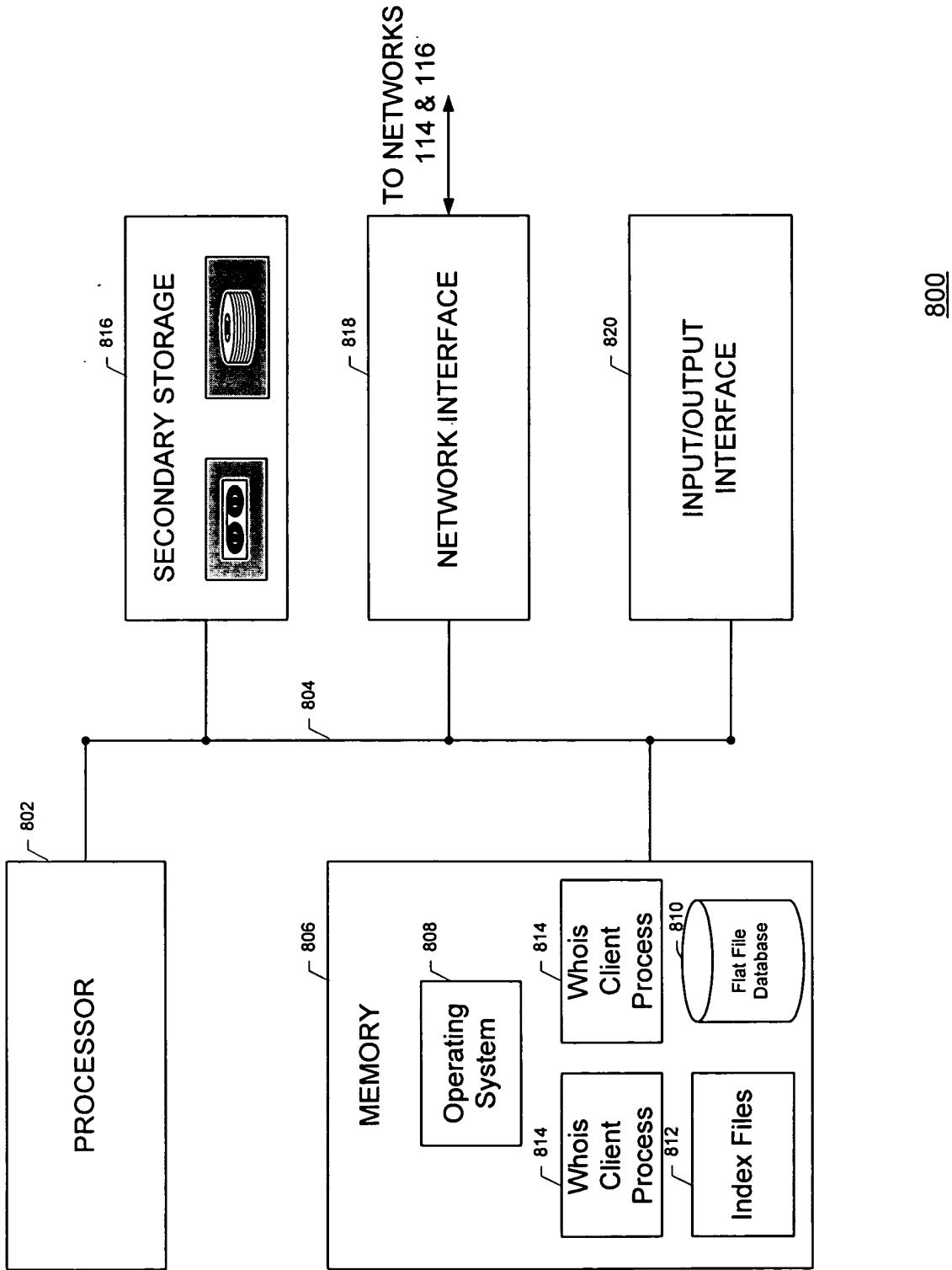


Figure 8